

EAST - [default.wsp:1]

File View Edit Tools Window Help

Pending

Active

L1: (58) (device or elements or fuse) same substrate same

L2: (6) semiconductor same substrate same element same

L3: (315) (semiconductor same substrate same element same

L4: (0) (chip or ic) same memory same pad same connect

L5: (8) (chip or ic) same memory same connect\$3 same

L6: (1790) 365/63.ccls.

L7: (197) 6 and fuse

L8: (114) 7 and substrate

Search

Lib

MPAGE

STATUS

Clear

DBs

US-PGPUB; USPAT; EPO: JPO

Default operator:

OR

Plurals

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	58	(device or elements or fuse) same substrate same program\$5 same (defect\$3 or bad) same second	US-PGPUB; USPAT: EPO: JPO
2	BRS	L2	6	semiconductor same substrate same element same fuse same surface same second same interconnect\$3	US-PGPUB; USPAT: EPO: JPO
3	BRS	L3	315	(semiconductor same substrate same element same fuse) and surface and second and (interconnect\$3 or connect\$3)	US-PGPUB; USPAT: EPO: JPO
4	BRS	L4	0	(chip or ic) same memory same pad same connect\$3 same fuse same decod\$3 same surface	US-PGPUB; USPAT: EPO: JPO
5	BRS	L5	8	(chip or ic) same memory same connect\$3 same fuse same decod\$3 same surface	US-PGPUB; USPAT: EPO: JPO
6	BRS	L6	1790	365/63.ccls	US-PGPUB; USPAT: EPO: JPO
7	BRS	L7	197	6 and fuse	US-PGPUB; USPAT: EPO: JPO
8	BRS	L8	114	7 and substrate	US-PGPUB; USPAT: EPO: JPO
9	BRS	L9	65	8 and surface	US-PGPUB; USPAT: EPO: JPO
10	BRS	L10	65	9 and connect\$3	US-PGPUB; USPAT: EPO: JPO
11	BRS	L11	64	10 and second	US-PGPUB; USPAT: EPO: JPO
12	BRS	L12	59	11 and element	US-PGPUB; USPAT: EPO: JPO
13	BRS	L13	31	12 and defect\$3	US-PGPUB; USPAT: EPO: JPO
14	BRS	L14	413	365/52.ccls.	US-PGPUB; USPAT: EPO: JPO
15	BRS	L15	28	14 and fuse	US-PGPUB; USPAT: EPO: JPO

Start

5:24